EverGreen mSATA

FEATURES

- High Performance
- S.M.A.R.T. & i-S.M.A.R.T. Supported
- MLC Flash with L^2 Architecture
- Cost-Efficiency
- Long Life : Warranty 3 years
- Compliant with JEDEC MO-300, SATA-IO mini SATA

R: 120MB W: 50MB | SATA II 3Gb/s | ECC wear leveling | Thermal sensor | L^2 Architecture | Dram buffer

Specifications

Connector Type	miniPCIe Pin Define for SATA Signal, 23:TX+; 25:TX-; 31:RX-; 33:RX+
Flash Type	MLC (Multi Level Cell)
Density	4GB, 8GB, 16GB, 32GB, 64B, 128GB
Interface	SATA II, SATA I, PIO 0~4, MDMA 0~2, UDMA 0~6
Sustained R/W Performance	Read：120MB/sec (max.) Write：50 MB/sec (max.)
User Capacity	Reserve 10% for L^2 algorithm

Environmental

DC Input	+3.3V DC ± 5%
Power consumption (Max.)	Read: 230mA Write: 300 mA Idle: 190 mA
Operating Temperature	0°C~+70°C (Standard Grade) -40°C~+85°C (Industrial Grade)
Storage Temperature	-55°C~+95°C
Humidity	Relative Humidity: 10-95%, non-condensing
Flash Endurance	3,000 program/erase cycles
MTBF	> 3,000,000 hours
Certification	CE, FCC, RoHS
Warranty	3 years

Mechanicals

Dimension (W x L x H)	29.85mm x 50.8mm x 3.5mm
Weight	9g±2g
Vibration	7 Hz to 2K Hz, , 3 axes
Shock	Duration: 20G/0.5ms, , 3 axes

Health monitoring Tool

S.M.A.R.T.	Supported
i-S.M.A.R.T.	Supported (Utility for Windows, Linux)

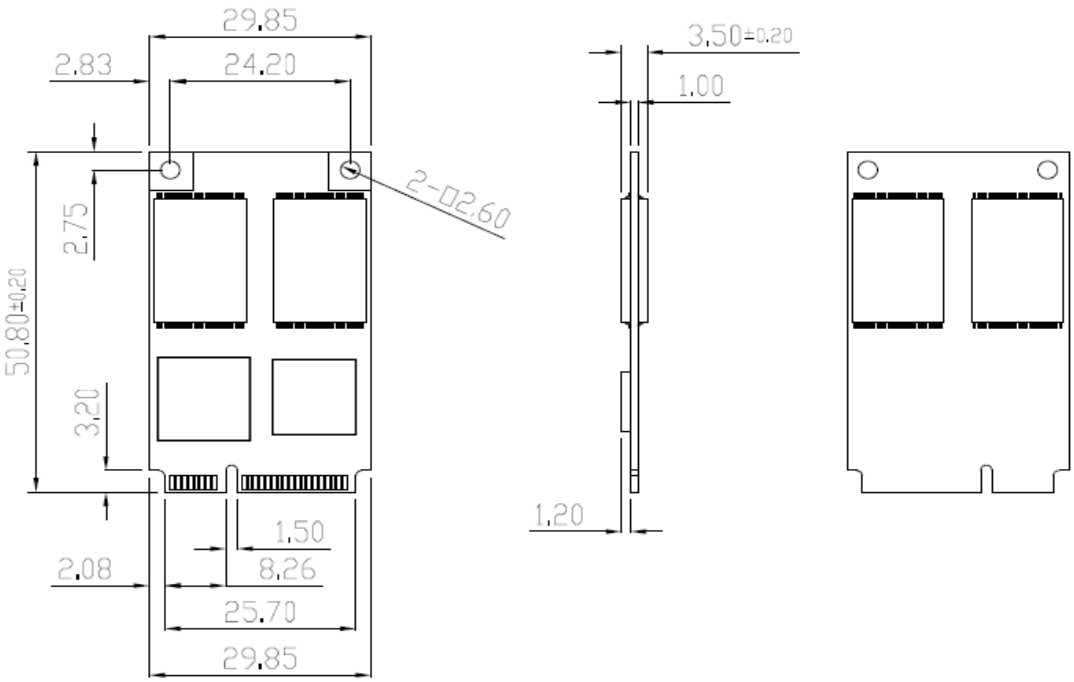
Ordering Information

Capacity	Standard Grade	Industrial Grade
4GB	DRPS-04GJ20BC1SN	DRPS-04GJ20BW1SN
8GB	DRPS-08GJ20BC1DN	DRPS-08GJ20BW1DN
16GB	DRPS-16GJ20BC1QN	DRPS-16GJ20BW1QN
32GB	DRPS-32GJ20BC1QN	DRPS-32GJ20BW1QN
64GB	DRPS-64GJ20BC1QN	DRPS-64GJ20BW1QN
128GB	DRPS-A28J20BC1QN	DRPS-A28J20BW1QN

DESIGN ◎ SERVICE ◎ QUALITY ◎ DELIVERY

Headquarter (Taiwan) : HTTP://WWW.INNODISK.COM E-MAIL:SALES@INNODISK.COM
TEL: +886-2-26963000 ADD: 9F., No.100, Sec. 1, Xintai 5th Rd., Xizhi Dist., New Taipei City, Taiwan(R.O.C.)
USA Office: usasales@innodisk.com / Europe: eusales@innodisk.com / Japan: jpsales@innodisk.com / China: sales_cn@innodisk.com

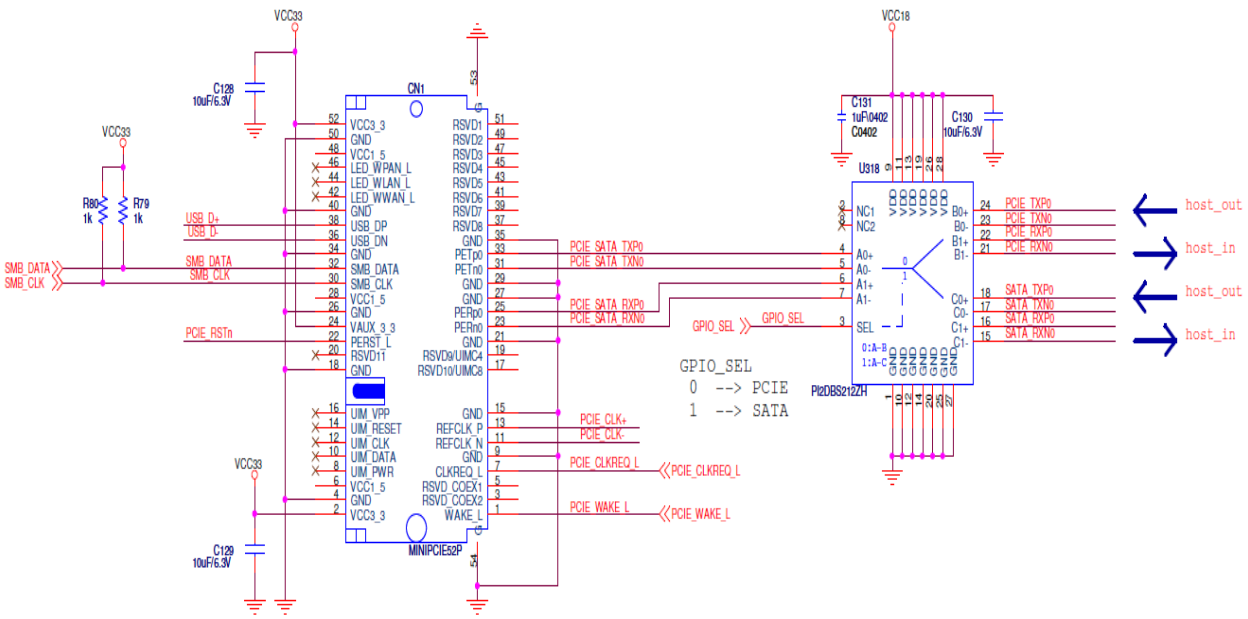
Mechanical Dimension



Tolerance ± 0.1

The pin assignment, component keep out area of EverGreen mSATA is followed by JEDEC MO300 Specification.

PCIe and SATA Signal Switch MB Reference Circuit Design



We suggested MB can be designed a "signal bus switch IC" to transfer the differential channels. Due to the bi-directional nature of bus switch IC, it can be used to allocate bandwidth between PCI express and SATA II signal. You can increase one more option for your miniPCIe device.