EDC 4000 VERTICAL

FEATURES

- Support static wear leveling algorithm
- S.M.A.R.T. & i-S.M.A.R.T. Supported
- Intelligent system for error recovery
- High reliability & compatibility
- Mechanical design for anti-vibration
- Enhanced power cycling

R: 40MB W: 28MB | **S.M.A.R.T** | **Power cycling enhance**

PIO 0~4 Support | **Wear Leveling** | **High Reliability**

Specifications

Connector Type	2.00 mm Pin Pitch (2x22) 2.54 mm Pin Pitch (2x20)
Flash Type	SLC (Single Level Cell)
Density	128MB~16GB
Transfer Mode	PIO 0~4, MDMA 0~4, UDMA 0~4
Sustained R/W Performance	Read：40MB/sec (max.) Write：28MB/sec (max.)

Environmental

DC Input	+3.3V/+5V DC ± 5%
Power consumption (Max.)	Read: 125 mA Write: 120 mA Idle: 1.3 mA
Operating Temperature	0°C~+70°C (Standard Grade) -40~+85°C (Industrial Grade)
Storage Temperature	-55°C~+95°C
Humidity	Relative Humidity: 10-95%, non-condensing
Flash Endurance	100,000 program/erase cycles
MTBF	> 3,000,000 hours
Certification	CE, FCC, RoHS
Warranty	5 years

Mechanicals

Dimension (W x L x H)	40 pin: 60.2 x 27.3 x 6.4 mm 44 pin: 50.3 x 27.3 x 5.8 mm
Weight	9g±1g
Vibration	7 Hz to 2K Hz, 20G, 3 axes
Shock	Duration: 0.5ms, 1500G, 3 axes

Health monitoring Tool

S.M.A.R.T.	Supported
i-S.M.A.R.T.	Supported (Utility for Windows, Linux)

Ordering Information

Capacity	Standard Grade	Industry Grade
128MB	DE0H-128D31C1SR DE4H-128D31C1SR	DE0H-128D31W1SR DE4H-128D31W1SR
256MB	DE0H-256D31C1SR DE4H-256D31C1SR	DE0H-256D31W1SR DE4H-256D31W1SR
512MB	DE0H-512D31C1SR DE4H-512D31C1SR	DE0H-512D31W1SR DE4H-512D31W1SR
1GB	DE0H-01GD31C1DR DE4H-01GD31C1DR	DE0H-01GD31W1DR DE4H-01GD31W1DR
2GB	DE0H-02GD31C1DR DE4H-02GD31C1DR	DE0H-02GD31W1DR DE4H-02GD31W1DR
4GB	DE0H-04GD31C1DR DE4H-04GD31C1DR	DE0H-04GD31W1DR DE4H-04GD31W1DR
8GB	DE0H-08GD31C1DR DE4H-08GD31C1DR	DE0H-08GD31W1DR DE4H-08GD31W1DR
16GB	DE0H-16GD31C1DT DE4H-16GD31C1DT	DE0H-16GD31W1DT DE4H-16GD31W1DT

DESIGN ◎ SERVICE ◎ QUALITY ◎ DELIVERY

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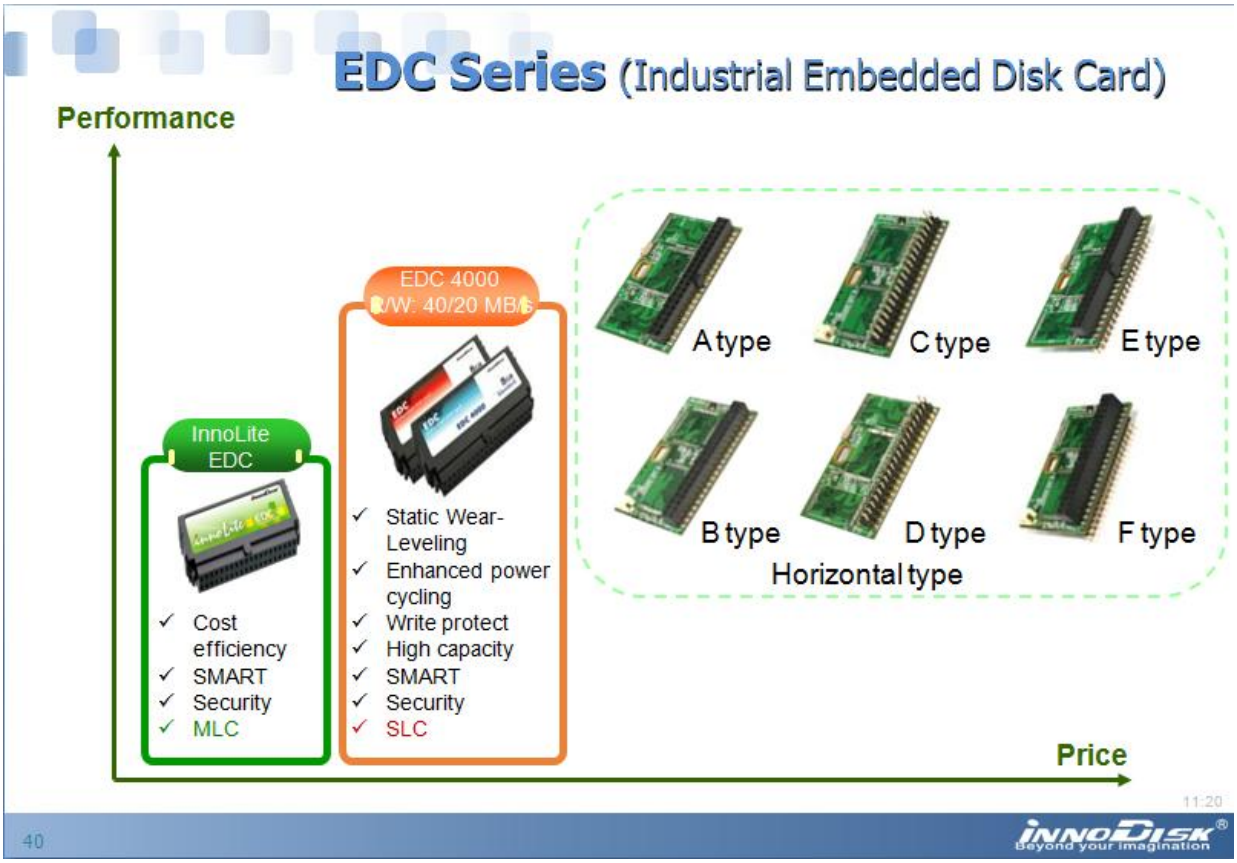

Power Cycling Management

InnoDisk's Power Cycling Management in the case of sudden power outage to an SSD. The majority of data protection takes place before power is cycled after an abnormal power failure. Any residual data corruption after system power-on is handled by data sorting algorithms to ensure data integrity in Flash storage.